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** CONTINUING DATA *****

This application is a DIV of 09/925,628 08/10/2001 PAT 6,690,070

** FOREIGN APPLICATIONS *****

JAPAN 2000-242617 08/10/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature	Initials		

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TITLE

Method of manufacturing insulated gate semiconductor device

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees
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